EE5285: Wireline Communication Integrated Circuits

Instructor:	Pen-Jui Peng
Office:	R803, Delta Building
E-mail:	pjpeng@ee.nthu.edu.tw

Course Description:

This is an introductory course of integrated circuits for wireline/optical communications. It covers basic modulation/demodulation schemes, system design considerations, and key function blocks. All material is based on modern VLSI implementation viewpoints.

Prerequisites: Microelectronics

Textbook: Class Notes and Technical Papers

Teaching Method:

Lecture: 3 hours Outside Study: 3 hours

References:

- 1. Design of Integrated Circuits for Optical Communications, B. Razavi, McGraw-Hill, 2003.
- 2. RF Microelectronics, B. Razavi, Prentice Hall, 1998.

Grading:

- 1. Homework: 40%
- 2. Final Exam: 30%
- 3. Final Project: 30%

Outline:

- 1. Introduction of Wireline Communication
- 2. Channel
- 3. Termination and Drivers
- 4. Mux/Demux and High-speed Logics
- 5. Adaptive Equalizers

*AI is not applicable in this course.